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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yu Chen, Joel A. Gerber, Brian E. Schrieber and Joshua W. Smith

Serial No.:

09/531,285

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For:

CIRCUIT ELEMENTS USING A

Z-AXIS INTERCONNECT

Group Art Unit: 2841

Examiner: J. Vigushin

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents Washington, DC 20231

Enclosed are the formal drawings (eight sheets) for the subject application.

Respectfully submitted,

Date: 5/4/2001

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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on:

Date: 5/4/2001

Signed: